

最佳論文獎

| 領域 | 名次 | 學校 | 獲獎同學 | 指導教授 | 論文名稱 |
|------------------|----|----------|------|------|--|
| Analog & RF | 1 | 國立臺灣科技大學 | 曾文丁 | 彭盛裕 | A Single-Inductor Thermoelectric Energy Harvesting Interface with Integrated Cold Start-up, MPPT, and Energy Reuse Circuitries |
| | 2 | 國立中興大學 | 陳映辰 | 楊清淵 | A 2.9-Gb/s Transceiver Using Phase Interpolation for Spread-Spectrum-Modulation Chip-to-Chip Links |
| | 3 | 國立臺灣科技大學 | 邱雋洋 | 鍾勇輝 | An 88.6-dB SNDR Discrete-Time Delta-Sigma Modulator Using Two-Stage Floating Inverter Amplifiers in 180-nm CMOS |
| Digital & System | 1 | 國立臺灣大學 | 林亮昕 | 楊家驤 | A Energy Efficient, Scalable MK-CKKS Processor for Secure Multi-Party Computation |
| | 2 | 國立中山大學 | 吳東霖 | 施信毓 | Low-Area ASIC Architecture of Fault-Aware High-Reliability Triple-Mode Polar Decoder Chip Reconfiguring SC and SCL Decoding |
| | 3 | 國立臺灣大學 | 石博允 | 吳安宇 | Post-Training Quantization for Vision Mamba with k-Scaled Quantization and Reparameterization |
| EDA & Testing | 1 | 國立臺灣大學 | 陳少翔 | 張耀文 | Pre-Assignment Routing for Performance Optimization in High-Speed 2.5D/3D Packaging |
| | 2 | 國立臺灣大學 | 陳天富 | 江介宏 | A Measure-and-Prepare Framework for Arbitrary Quantum Gate Excision and Its Applications in Near-Clifford Circuit Simulation |
| | 3 | 國立臺灣大學 | 梁峻璋 | 江蕙如 | Optical Logic Synthesis for Efficiency Optimization via Harmonic Mean and Integer Partition |